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DEC 19 2005

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: Norris et al.

Docket No.: FIS920030305

Serial No.: 10/605,927

Art Unit: 2138

Filed: November 6, 2003

Examiner: Phung M Chung

Title: METHOD FOR PERFORMING A BURN-IN TEST

**RESPONSE TO RESTRICTION REQUIREMENT**

Commissioner for Patents & Trademarks  
P. O. Box 1450  
Alexandria, VA 22313-1450

In response to the Restriction Requirement mailed on December 13, 2005, Applicant hereby provisionally elects Group I, claims 1-7 and claims 15-20, drawn to a method of testing a DDR DRAM having a test mode and an operational mode, classified in class 714, subclass 719. This election is made with traverse.

The Examiner stated "Group I does not require peripheral logic circuits of the DDR DRAM are adapted to execute a write burst enable and a column address command one clock cycle earlier in test mode than in operational mode, adapted to execute an auto-precharge enable one-half clock cycle earlier in test mode than in operational mode, and having a column address latency of one clock cycle in test mode and two or three clock cycles in operational mode."

Applicants, respectfully point out that claim 6 of Group I does include this limitation.

Therefore, with regard to the Restriction Requirement, Applicants respectfully submit that the subject matter of all claims 1-20 is sufficiently related that a thorough search for the subject matter of any one group of claims would encompass a search for the subject matter of the remaining claims. Thus, Applicants respectfully submit that the search and the examination of

the entire application could be made without serious burden. See MPEP § 803, in which it is stated "if the search and examination of the entire application can be made without serious burden, the Examiner must examine it on the merits, even though it includes claims to independent or distinct inventions" (emphasis added). Applicants respectfully submit that this policy should apply in the present application in order to avoid unnecessary delay and expense to Applicants and duplicative examination by the Patent Office.

The Director is hereby authorized to charge and/or credit Deposit Account 09-0458.

Should the Examiner require or request anything further from Applicant's prior to examination, the Examiner is requested to contact Applicants' undersigned representative at the Agent Direct Dial telephone number below. Otherwise, Applicants request early and favorable examination on the merits

Respectfully submitted,  
FOR: Norris et al.

Dated: 12/19/2005

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